Docket No.: J0658.0014
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Robert E. Ober et al.

Application No.: 10/712,473	Confirmation No.: 9335
Filed: November 12, 2003	Art Unit: 2183
For: INTERRUPT AND TRAP HANDLING IN AN EMBEDDED MULTI-THREAD PROCESSOR TO AVOID PRIORITY INVERSION AND MAINTAIN REAL-TIME OPERATION	· Examiner: B. P. Johnson

SUBMISSION OF REVOCATION OF POWER OF ATTORNEY WITH NEW POWER OF ATTORNEY AND CHANGE OF CORRESPONDENCE ADDRESS, AND STATEMENT UNDER 37 CFR 3.73(B)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Submitted herewith are (1) two Revocations of Power of Attorney with New Power of Attorney and Change of Correspondence Address, and (2) Statement Under 37 CFR 3.73(b). Entry of these documents is respectfully requested.

Dated: May 19, 2006

Respectfully submitted,

By _____
Laura C. Brutman
Registration No.: 38,395
DICKSTEIN SHAPIRO MORIN & OSHINSKY LLP
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PTO/SB/96 (12-05)
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STATEMENT UNDER 37 CFR 3.73(b)

Applicant/Patent Owner: INFINEON TECHNOLOGIES AG

Application No./Patent
No./Control No.:   10/712,473   Filed/Issue Date: November 12, 2003

Entitled: INTERRUPT AND TRAP HANDLING IN AN EMBEDDED MULTI-THREAD PROCESSOR TO AVOID PRIORITY INVERSION AND MAINTAIN REAL-TIME OPERATION

    INFINEON TECHNOLOGIES AG   , a   Corporation
     (Name of Assignee)   (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1.   [X]   the assignee of the entire right, title, and interest; or

2.   [ ]   an assignee of less than the entire right, title and interest.
         (The extent (by percentage) of its ownership interest is   %)
in the patent application/patent identified above by virtue of either:

A.   [ ]   An assignment from the inventor(s) of the patent application/patent identified above. The assignment
         was recorded in the United States Patent and Trademark Office at Reel   ,
         Frame   , or a true copy of the original assignment is attached.

OR

B.   [X]   A chain of title from the inventor(s), of the patent application/patent identified above, to the current
         assignee as follows:

     1. From: Ober, Robert E.   To: Infineon Technologies North America Corp.
         The document was recorded in the United States Patent and Trademark Office at
         Reel   014705   , Frame   0702   , or for which a copy thereof is attached.

     2. From: Infineon Technologies North America Corp.   To: INFINEON TECHNOLOGIES AG
         The document was recorded in the United States Patent and Trademark Office at
         Reel   014874   , Frame   0871   , or for which a copy thereof is attached.

     3. From: To:
         The document was recorded in the United States Patent and Trademark Office at
         Reel   , Frame   , or for which a copy thereof is attached.

     [ ]   Additional documents in the chain of title are listed on a supplemental sheet.

**As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner
to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.**
[NOTE: A separate copy (*i.e.*, a true copy of the original assignment document(s)) must be submitted to
Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO.
See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

_____   May 19, 2006
         Signature   Date

    Laura C. Brutman   212-835-1400
    Printed or Typed Name   Telephone Number

    Authorized Signer for Assignee
         Title

OIPE
MAY 19 2006
PATENT & TRADEMARK OFFICE

REVOCATION OF POWER OF ATTORNEY WITH NEW POWER OF ATTORNEY AND CHANGE OF CORRESPONDENCE ADDRESS

I hereby <u>revoke</u> all previous powers of attorney given in the below-identified applications and patents.

I hereby <u>appoint</u> the practitioners associated with Customer Number: <u>38881</u>.

Please change the <u>correspondence address</u> for the below-identified applications and patents to the address associated with Customer Number: <u>38881</u>.

I am the assignee of record of the entire interest in each of the below-identified applications and patents. Statements under 37 CFR 3.73(b) for each of the respective applications/patents are enclosed with the submitted copies of this document.

Authorization to file this Revocation of Power of Attorney with New Power of Attorney and Change of Correspondence Address form listing multiple application and patent numbers rather than the assignee, Infineon Technologies North America Corp., signing a separate form for each application and patent was provided by Magdalen Y. Greenlief (tel.: 571-272-8850) of the Office of the Manual of Patent Examining Procedure. The original copy of this document is filed in Application No. 10/774,193, which was filed on February 6, 2004.

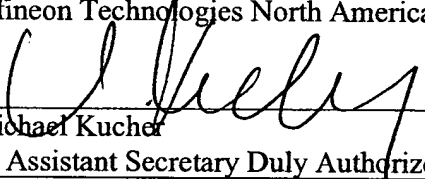| Appln. No.	Filing Date	Title	Inventor(s)
*10/712,473	11/12/2003	INTERRUPT AND TRAP HANDLING IN AN EMBEDDED MULTI-THREAD PROCESSOR TO AVOID PRIORITY INVERSION AND MAINTAIN REAL-TIME OPERATION	Ober, Robert E.; Arnold, Roger D.; Martin, Daniel F.; Norden, Erik K.
*11/130,552	5/16/2005	CONTROLLING OUT OF ORDER EXECUTION PIPELINES USING PIPELINE SKEW PARAMETERS	Norden, Erik K.; Arnold, Roger D.; Ober, Robert E.; Hastie, Neil S.
10/386,974	3/12/2003	MULTIPLE DELAY LOCKED LOOP INTEGRATION SYSTEM AND METHOD	Zweck, Harald
10/396,063	3/24/2003	ZERO-OVERHEAD LOOP OPERATION IN MICROPROCESSOR HAVING INSTRUCTION BUFFER	Ahmad, Sagheer; Knoth, Matthias; Arnold, Roger D.
10/411,409	4/9/2003	NEW ARITHMETIC LOGIC UNIT WITH MERGED CIRCUITRY FOR COMPARISON, MINIMUM/MAXIMUM SELECTION AND SATURATION FOR SIGNED AND UNSIGNED NUMBERS	Griessing, Alexander M.
10/444,918	5/22/2003	CONFIGURABLE REAL-TIME TRACE PORT FOR EMBEDDED PROCESSORS	Ahmad, Sagheer; Ober, Robert E.
10/705,101	11/10/2003	MEMORY DEBUGGER FOR SYSTEM-ON-A-CHIP DESIGNS	Oberlaender, Klaus J.
10/714,137	11/13/2003	MACHINE INSTRUCTION FOR ENHANCED CONTROL OF MULTIPLE VIRTUAL PROCESSOR SYSTEMS	Arnold, Roger D.; Ober, Robert E.
10/774,193	2/6/2004	PROGRAM TRACING IN A MULTITHREADED PROCESSOR	Ober, Robert E.; Martin, Daniel F.; Arnold, Roger D.; Norden, Erik K.

DOCSNY.190221.1

*Infineon Technologies North America is assignee of record of a *partial* interest of these applications/patents.

Appln. No.	Filing Date	Title	Inventor(s)
10/774,226	2/6/2004	THREAD ID IN A MULTITHREADED PROCESSOR	Norden, Erik K.; Ober, Robert E.; Arnold, Roger D.; Martin, Daniel F.
10/777,570	2/11/2004	FAST UNALIGNED MEMORY ACCESS SYSTEM AND METHOD	Oberlaender, Klaus J.; Norden, Erik K.
10/777,739	2/11/2004	CONFIGURABLE EMBEDDED PROCESSOR	Ober, Robert E.; Oberlaender, Klaus J.
10/777,863	2/11/2004	CONFIGURABLE MEMORY SYSTEM FOR EMBEDDED PROCESSORS	Oberlaender, Klaus J.; Haines, Ralph
11/114,734	4/25/2005	MULTICAST/BROADCAST EXTENSION TO A POINT-TO-POINT UNICAST-ONLY PACKET SWITCH SYSTEM	Lin, Jim; Kadamby, Vijayasimha

Patent No.	Issue Date	Title	Inventor(s)
*6,774,691	8/10/2004	HIGH RESOLUTION INTERLEAVED DELAY CHAIN	Han, Jonghee; Kim, Jung Pill
6,674,684	1/6/2004	MULTI- BANK CHIP COMPATIBLE WITH A CONTROLLER DESIGNED FOR A LESSER NUMBER OF BANKS AND METHOD OF OPERATING	Shen, William Wu
6,914,844	7/5/2005	DEEP POWER DOWN SWITCH FOR MEMORY DEVICE	Suh, Jungwon

Signature of Assignee of Record

Infineon Technologies North America Corp.	
Michael Kucher Its Assistant Secretary Duly Authorized	4/12/06 Date

REVOCATION OF POWER OF ATTORNEY WITH NEW POWER OF ATTORNEY AND CHANGE OF CORRESPONDENCE ADDRESS

I hereby <u>revoke</u> all previous powers of attorney given in the below-identified applications and patents.

I hereby <u>appoint</u> the practitioners associated with Customer Number: <u>38881</u>.

Please change the <u>correspondence address</u> for the below-identified applications and patents to the address associated with Customer Number: <u>38881</u>.

I am the assignee of record of the entire interest (unless noted otherwise) in each of the below-identified applications and patents. Statements under 37 CFR 3.73(b) for each of the respective applications/patents are enclosed with the submitted copies of this document.

Authorization to file this Revocation of Power of Attorney with New Power of Attorney and Change of Correspondence Address form listing multiple application and patent numbers rather than the assignee, Infineon Technologies AG, signing a separate form for each application and patent was provided by Magdalen Y. Greenlief (tel.: 571-272-8850) of the Office of the Manual of Patent Examining Procedure. The original copy of this document is filed in Application No. 11/391,666, which was filed on March 27, 2006.

Appln. No.	Filing Date	Title	Inventor(s)
*10/712,473	11/12/2003	INTERRUPT AND TRAP HANDLING IN AN EMBEDDED MULTI-THREAD PROCESSOR TO AVOID PRIORITY INVERSION AND MAINTAIN REAL-TIME OPERATION	Ober, Robert E.; Arnold, Roger D.; Martin, Daniel F.; Norden, Erik K.
*10/727,102	12/2/2003	ARRANGEMENT COMPRISING A FIRST SEMICONDUCTOR CHIP AND A SECOND SEMICONDUCTOR CHIP CONNECTED THERETO	Barrenscheen, Jens; Rohm, Peter; Rohm, Angela; Estl, Hannes; Aue, Axel; Graf, Jens; Roozenbeek, Herman
*10/727,108	12/2/2003	ARRANGEMENT COMPRISING A FIRST SEMICONDUCTOR CHIP AND A SECOND SEMICONDUCTOR CHIP	Barrenscheen, Jens; Rohm, Peter; Aue, Axel; Graf, Jens; Roozenbeek, Herman
*10/868,502	6/14/2004	KEYPAD IN TEXTILES WITH CAPACITIVE READ-OUT CIRCUIT	Jung, Stefan; Lauterbach, Christl
*11/078,202	3/10/2005	CHIP CARD	Puschner, Frank; Proske, Reinhard; Stampka, Peter; Muller-Hipper,

Appln. No.	Filing Date	Title	Inventor(s)
*11/130,552	5/16/2005	CONTROLLING OUT OF ORDER EXECUTION PIPELINES USING PIPELINE SKEW PARAMETERS	Andreas
*11/206,241	8/16/2005	CHIP MODULE	Norden, Erik K.; Arnold, Roger D.; Ober, Robert E.; Hastie, Neil S.
08/872,680	6/10/1997	METHOD AND APPARATUS FOR COMPILING ONE CIRCUIT IN A SEQUENCE OF CIRCUITS WITHIN A PROGRAMMABLE GATE ARRAY	Puschner, Frank; Dengler, Dietmar; Schindler, Wolfgang; Spottl, Thomas
09/492,634	1/27/2000	IMPROVED APPARATUS AND METHOD FOR MULTI-THREADED SIGNAL PROCESSING	Cummings, Mark R.; Watson, John L.
09/565,646	5/5/2000	APPARATUS AND METHOD FOR ORTHOGONAL BUS TRANSPORT	Subramanian, Ravi; Rieken, Keith
09/565,649	5/5/2000	PROGRAMMABLE DATA ARITHMETIC ARRAYS	Wasson, Stephen L.
09/565,652	5/5/2002	APPARATUS AND METHOD FOR DESIGNING A CIRCUIT USING MINIMUM SLICE CONSTRUCTION AND REPLICATION	Wasson, Stephen L.
09/565,655	5/5/2000	PROGRAMMABLE LOGIC DEVICE WITH A PROGRAMMABLE SECURITY CIRCUIT	Wasson, Stephen L.
09/751,777	12/29/2000	A FAST INITIAL ACQUISITION/SEARCH METHODOLOGY FOR CDMA AND W-CDMA SYSTEMS	Wasson, Stephen L.; Varn, David K.; Ralston, John D.
09/772,583	1/29/2001	METHOD AND APPARATUS FOR PROCESSING A SECONDARY SYNCHRONIZATION CHANNEL IN A SPREAD SPECTRUM SYSTEM	Medlock, Joel D.
09/772,584	1/29/2001	A WIRELESS SPREAD SPECTRUM COMMUNICATION PLATFORM USING DYNAMICALLY RECONFIGURABLE LOGIC	Jha, Uma
09/828,381	4/5/2001	VIRTUAL MACHINE INTERFACE FOR HARDWARE RECONFIGURABLE AND SOFTWARE PROGRAMMABLE PROCESSORS	Subramanian, Ravi; Jha, Uma; Medlock, Joel D.
09/912,721	7/24/2001	DISTRIBUTED MICRO INSTRUCTION SET PROCESSOR ARCHITECTURE FOR HIGH-EFFICIENCY SIGNAL PROCESSING	Chen, Song; Hesky, Kenneth M.; Joag, Raju R.; Medlock, Joel D.; Woodthorpe, Christopher C.
09/919,700	7/31/2001	METHOD AND APPARATUS FOR CONFIGURABLE MULTI-DWELL SEARCH ENGINE FOR CDMA APPLICATIONS	Chen, Song; Chou, Paul L.; Woodthrope, Christopher C.; Balasubramonian, Venugopal; Rieken, Keith
09/920,093	7/31/2001	METHOD AND APPARATUS FOR TIME-SLICED AND MULTI-THREADED DATA PROCESSING IN A COMMUNICATION SYSTEM	Rieken, Keith
09/920,094	7/31/2001	GENERIC FINGER ARCHITECTURE WCDMA APPLICATIONS	Rieken, Keith; Medlock, Joel D.; Holmes, David M.
09/920,095	7/31/200	METHOD AND APPARATUS FOR SAMPLE SELECTION AND REUSE OF RAKE FINGERS IN SPREAD SPECTRUM SYSTEMS	Subramanian, Ravi; Rieken, Keith; Jha, Uma; Holmes, David M.; Medlock, Joel D.; Krishnan, Murali
09/922,484	8/3/2001	DYNAMICALLY RECONFIGURABLE TRANSMITTER SYSTEM	Medlock, Joel D.; Rieken, Keith; Holmes, David M. 09/920,095
09/922,486	8/3/2001	FLEXIBLE TDMA SYSTEM ARCHITECHTURE	Medlock, Joel D.
09/927,906	8/9/2001	METHOD AND APPARATUS FOR SOFTWARE-BASED ALLOCATION AND SCHEDULING OF HARDWARE RESOURCES IN A WIRELESS COMMUNICATION DEVICE	Subramanian, Ravi; Woodthrope, Christopher C.; Holmes, David M.; Jha, Uma
			Kavoori, Chakki; Rieken, Keith; Holmes, David M.

Appln. No.	Filing Date	Title	Inventor(s)
09/928,273	8/9/2001	ARCHITECTURE AND PROGRAMMING INTEFACE FOR EFFICIENT SOFTWARE DOWNLOAD	Ralston, John D.; Chen, Song; Subramanian, Ravi; Williams, Ted E.
10/040,727	12/28/2001	CHANNEL CODEC PROCESSOR CONFIGURABLE FOR MULTIPLE WIRELESS COMMUNICATIONS STANDARDS	Bitterlich, Stefan Johannes; Holmes, David Morris
10/133,145	5/7/1999	HETEROGENEOUS PROGRAMMABLE GATE ARRAY	Wasson, Stephen L.
10/212,190	8/2/2002	CONFIGURABLE TERMINAL ENGINE	Gavnoudias, Stratis; Boccuzzi, Joseph; Jacques, Alexander; Misra, Raj; Zhang, Yan; Silverberg, Avi; Rizza, Rich; Nacson, Yosef
10/226,666	8/21/2002	METHOD AND APPARATUS FOR ENHANCING DATA RATES IN SPREAD SPECTRUM COMMUNICATION SYSTEMS	Jalloul, Louay; Shanbhag, Abhijit G.
10/231,384	8/29/2002	INTEGRATED CIRCUIT CHIP DESIGN	Williams, Ted E.; Ferro, Jonathan; Tovey, Deforest; Tseng, Louis
10/243,173	9/12/2002	CDMA WIRELESS SYSTEMS	Naguib, Ayman F.
10/317,875	12/11/2002	DIGITAL PROCESSOR WITH PROGRAMMABLE BREAKPOINT/WATCHPOINT TRIGGER	Ahmad, Sagheer; Shirwal, Anand
10/338,625	1/6/2003	APPARATUS AND METHOD FOR FAST HADAMARD TRANSFORMS	Medlock, Joel D.
10/431,996	5/7/2003	MULTI-THREADED EMBEDDED PROCESSOR USING DETERMINISTIC INSTRUCTION MEMORY TO GUARANTEE EXECUTION OF PRE-SELECTED THREADS DURING BLOCKING EVENTS	Ober, Robert E.; Arnold, Roger D.; Martin, Daniel; Norden, Erik K.
10/442,642	5/20/2003	A CONFIGURABLE CODE GENERATOR SYSTEM FOR SPREAD SPECTRUM APPLICATIONS ...	Medlock, Joel D.; Chou, Paul L.
10/475,599	4/23/2004	PROGRAM-CONTROLLED UNIT	Maier, Klaus D.; Konig, Dietmar; Kolof, Andreas; Mayer, Albrecht
10/475,600	5/6/2004	PROGRAM-CONTROLLED UNIT	McDonald-Maier, Klaus D.; Konig, Dietmar; Kolof, Andreas; Mayer, Albrecht
10/478,751	6/8/2004	METHOD FOR PRODUCING A LAYER WITH A PREDEFINED LAYER THICKNESS PROFILE	Aigner, Robert; Elbrecht, Luder; Marksteiner, Stephan; Nessler, Winfried; Timme, Hans-Jorg
10/490,230	10/18/2004	PROGRAMME-CONTROLLED UNIT	Rohm, Peter; Rohm, Angela
10/490,992	9/24/2004	METHOD FOR STORING OR TRANSFERRING DATA	Baradie, Sammy; McDonald-Maier, Klaus D.; Mayer, Albrecht
10/491,070	10/18/2004	ELECTRICAL CIRCUIT	Baradie, Sammy; Kaibel, Martin; Weisgerber, Andreas
10/491,072	9/27/2004	PROGRAMMABLE UNIT	Von Wendorff, Christophorus W.
10/701,113	11/3/2003	BIOCHIP FOR THE CAPACITIVE STIMULATION AND/OR DETECTION OF BIOLOGICAL TISSUE AND A METHOD FOR ITS PRODUCTION	Schreiter, Matthias; Gabl, Reinhard; Jenkner, Martin; Eversmann, Bjorn; Hofmann, Franz
10/712,767	11/12/2003	PROCESS FOR PRODUCING A NANOELEMENT ARRANGEMENT, AND NANOELEMENT ARRANGEMENT	Unger, Eugene; Dusberg, Georg

Appln. No.	Filing Date	Title	Inventor(s)
			Stefan; Graham, Andrew; Liebau, Maik
10/714,536	11/13/2003	POLYMER TRANSISTOR ARRANGEMENT, INTEGRATED CIRCUIT ARRANGEMENT AND METHOD FOR PRODUCING A POLYMER TRANSISTOR ARRANGEMENT	Brederlow, Ralf
10/714,536	11/13/2003	ELECTRONIC CIRCUIT WITH ASYNCHRONOUS CLOCKING OF PERIPHERAL UNITS	Elbe, Astrid; Janssen, Norbert; Sedlak, Holger
10/723,309	11/26/2003	CIRCUIT ARRANGEMENT	Pacha, Christian; Thewes, Roland; Armim, Klaus von
10/723,432	11/25/2003	ELECTRONIC CIRCUIT WITH ASYNCHRONOUS CLOCKING OF PERIPHERAL UNITS	Elbe, Astrid; Janssen, Norbert; Sedlak, Holger
10/723,448	11/25/2003	PROCESSOR WITH INTERNAL MEMORY CONFIGURATION	Elbe, Astrid; Janssen, Norbert
10/724,016	11/25/2003	ELECTRONIC CIRCUIT WITH ENERGY CONTROL	Elbe, Astrid; Janssen, Norbert; Sedlak, Holger
10/727,101	12/2/2003	ARRANGEMENT CONSISTING OF A PROGRAM-CONTROLLED UNIT AND A POWER CHIP CONNECTED TO IT	Barrenscheen, Jens; Rohm, Peter
10/735,517	12/11/2003	PREVENTING THE UNWANTED EXTERNAL DETECTION OF OPERATIONS IN DIGITAL INTEGRATED CIRCUITS	Eckstein, Gernot; Aumueller, Christian; Wallstab, Stefan
10/742,601	12/19/2003	PHOTON EMITTER AND DATA TRANSMISSION DEVICE	Michler, Peter
10/745,473	12/22/2003	PIPETTING DEVICE AND METHOD FOR PRODUCING THE SAME	Fritz, Michaela; Dertinger, Stephan; Fuchs, Karin; Haneder, Thomas; Hanke, Hans-Christian; Jenkner, Martin; Lehmann, Volker; Paulus, Christian
10/750,532	12/30/2003	DYNAMICALLY RECONFIGURABLE TRANSMITTER SYSTEM	Medlock, Joel D.
10/750,619	12/30/2003	ADAPTIVE SEARCHER THRESHOLD SETTING USING CONSECUTIVE ISCP MEASUREMENTS	Jalloul, Louay; Kohlmann, Michael
10/750,924	12/31/2003	SIGNAL-TO-INTERFERENCE RATIO ESTIMATION FOR CDMA	Jalloul, Louay; Kohlmann, Michael; Medlock, Joel D.
10/754,909	1/9/2004	MEASURING CELL AND MEASURING FIELD COMPRISING MEASURING CELLS OF THIS TYPE, AND USE OF A MEASURING FIELD	Brederlow, Ralf; Eversmann, Bjorn-Oliver; Koren, Ivo; Paulus, Christian; Thewes, Roland
10/755,971	1/12/2004	DATA-AIDED CHANNEL ESTIMATION	Jalloul, Louay; Misra, Raj M.
10/760,108	1/16/2004	METHOD FOR CONTROLLING A CENTRAL PROCESSING UNIT FOR ADDRESSING IN RELATION TO A MEMORY AND CONTROLLER	Freiwald, Juergen; Rabe, Dirk
10/762,882	1/21/2004	MAKING CONTACT WITH SEMICONDUCTOR CHIPS IN CHIP CARDS	Heinemann, Erik; Pueschner, Frank
10/764,265	1/23/2004	PROCESSOR WITH SEVERAL CALCULATING UNITS	Janke, Marcus; Laakmann, Peter
10/768,971	1/30/2004	A FIN FIELD-EFFECT TRANSISTOR AND METHOD FOR PRODUCING A FIN FIELD- EFFECT TRANSISTOR	Hofmann, Franz; Kretz, Johannes; Roesner, Wolfgang; Schulz, Thomas
10/769,286	1/30/2004	SECURITY MEMORY CARD AND PRODUCTION METHOD	Boker, Thorsten
10/773,114	2/4/2004	MICROELECTROMECHANICAL DEVICE AND METHOD FOR PRODUCING IT	Bottner, Harald; Schubert, Axel; Nurnus, Joachim; Jaegle, Martin
10/774,038	2/6/2004	THREAD SELECTION UNIT AND METHOD TO FAIRLY ALLOCATE	Arnold, Roger D.;

Appln. No.	Filing Date	Title	Inventor(s)
		PROCESSOR CYCLES IN A BLOCK MULTITHREADED PROCESSOR	Martin, Daniel F.; Ober, Robert E.; Norden, Erik K.
10/774,348	2/5/2004	CHIP CARD HAVING A PROJECTION MIRROR	Laackmann, Peter; Rueping, Stefan
10/777,710	2/11/2004	FAST UNALIGNED CACHE ACCESS SYSTEM AND METHOD	Oberlaender, Klaus J.
10/777,739	2/11/2004	CONFIGURABLE EMBEDDED PROCESSOR	Ober, Robert E.; Oberlaender, Klaus J.
10/781,359	2/17/2004	IC-CHIP HAVING A PROTECTIVE STRUCTURE	Aumuller, Christian; Janke, Marcus
10/789,373	2/27/2004	DEVICE AND METHOD FOR CALCULATING A RESULT OF MODULAR EXPONENTIATION	Seifert, Jean-Pierre; Velten, Joachim
10/789,423	2/27/2004	SOLDER, MICROELECTROMECHANICAL COMPONENT AND DEVICE, AND A PROCESS FOR PRODUCING A COMPONENT OR DEVICE	Bottner, Harald; Schubert, Axel; Jaegle, Martin
10/794,588	3/5/2004	MONOLITHIC INTEGRATED CIRCUIT ARRANGEMENT	Schindler, Dr. Gunthe
10/803,174	3/16/2004	NONCONDUCTING SUBSTRATE, FORMING A STRIP OR A PANEL, ON WHICH A MULTIPLICITY OF CARRIER ELEMENTS ARE FORMED	Barchmann, Bernd; Heinemann, Erik; Heitzer, Josef; Puschner, Frank
10/803,175	3/16/2004	FLUORESCENCE BIOSENSOR CHIP AND FLUORESCENCE BIOSENSOR CHIP ARRANGEMENT	Schienle, Meinrad; Brederlow, Ralf; Hofmann, Franz; Jenkner, Martin; Luyken, Richard Johannes; Paulus, Christian; Schindler-Bauer, Petra; Thewes, Roland
10/805,670	3/19/2004	SEMICONDUCTOR MEMORY ELEMENT ARRANGEMENT	Hofmann, Franz; Luyken, Richard Johannes; Specht, Michael
10/812,830	3/29/2004	A DEVICE AND METHOD FOR SOLDERING CONTACTS ON SEMICONDUCTOR CHIPS	Bergmann, Robert; Hubner, Holger
10/821,803	4/8/2004	BIOSENSOR CIRCUIT AND SENSOR ARRAY CONSISTING OF A PLURALITY OF SAID BIOSENSOR CIRCUITS AND BIOSENSOR ARRAY	Eversmann, Bjorn-Oliver; Jenkner, Martin; Paulus, Christian; Thewes, Roland
10/825,625	4/15/2004	METHOD AND APPARATUS FOR PROTECTING AN EXPONENTIATION CALCULATION BY MEANS OF THE CHINESE REMAINDER THEOREM (CRT)	Fischer, Wieland; Seifert, Jean-Pierre
10/826,881	4/16/2004	SENSOR ARRANGEMENT	Eversmann, Bjorn-Oliver; Paulus, Christian; Stromberg, Guido; Thewes, Roland
10/827,913	4/19/2004	METHOD AND APPARATUS FOR PROTECTING A CALCULATION IN A CRYPTOGRAPHIC ALGORITHM	Fischer, Wieland; Seifert, Jean-Pierre
10/831,455	4/23/2004	INTEGRATED CIRCUIT AND METHOD FOR FABRICATING AN INTEGRATED CIRCUIT	Mayer, Albrecht
10/846,483	5/13/2004	SEMICONDUCTOR CHIP WITH FIB PROTECTION	Ungar, Franz; Schmid, Gunter
10/848,997	5/18/2004	CONFIGURABLE EMBEDDED PROCESSOR	Oberlaender, Klaus J.; Haines, Ralph; Chesters, Eric; Behrens, Dirk
10/854,928	5/26/2004	INTEGRATED CIRCUIT AND METHOD FOR OPERATING THE INTEGRATED CIRCUIT	Sedlak, Holger; Kunemund, Thomas

Appln. No.	Filing Date	Title	Inventor(s)
10/854,932	5/26/2004	METHOD AND APPARATUS FOR MAPPING AN INPUT VALUE TO BE MAPPED TO AN ENCRYPTED MAPPED OUTPUT VALUE	Sonnekalb, Steffen Marc
10/857,616	5/27/2004	INTEGRATED CIRCUIT AND METHOD FOR OPERATING THE INTEGRATED CIRCUIT	Kunemund, Thomas
10/857,617	5/27/2004	METHOD FOR SYNCHRONIZING A CACHE MEMORY WITH A MAIN MEMORY	Gammel, Berndt; Kunemund, Thomas; Sedlak, Holger
10/857,635	5/28/2004	CIRCUIT ELEMENT HAVING A FIRST LAYER OF AN ELECTRICALLY INSULATING SUBSTRATE MATERIAL AND METHOD FOR MANUFACTURING A CIRCUIT ELEMENT	Weber, Dr. Werner; Schmid, Dr. Gunter; Luyken, J Richard; Thewes, Dr. Roland; Seitz, Dr. Markus
10/861,315	6/4/2004	METHOD OF MAKING CONTACT WITH CONDUCTIVE FIBERS	Jung, Stefan; Lauterbach, Christl
10/861,316	6/4/2004	APPARATUS AND METHOD FOR CONNECTING AN ELECTRIC CONDUCTOR ELECTRICALLY TO AN ELECTRONIC COMPONENT	Jung, Stefan; Lauterbach, Christl
10/864,822	6/8/2004	DATA PROCESSING DEVICE	Eckstein, Gernot; Kunemund, Thomas; Sedlak, Holger
10/866,070	6/10/2004	CIRCUIT ARRANGEMENT AND DATA PROCESSING METHOD	Pockrandt, Wolfgang
10/868,501	6/14/2004	CONSTRUCTION AND CONNECTION TECHNIQUE IN TEXTILE STRUCTURES	Jung, Stefan; Lauterbach, Christl
10/871,523	6/17/2004	TEST READING APPARATUS FOR MEMORIES	Dirscherl, Gerd; Sedlak, Holger; Schlager, Tobias
10/871,867	6/18/2004	INDUCTIVE COMPONENT	Tiebout, Marc
10/874,687	6/22/2004	DEVICE AND METHOD FOR ENCRYPTING DATA	Fischer, Wieland; Seifert, Jean-Pierre
10/890,523	7/12/2004	METHOD AND APPARATUS FOR PRODUCING A SHEET-LIKE STRUCTURE	Jung, Stefan; Lauterbach, Christl
10/893,161	7/16/2004	SHIFT DEVICE AND METHOD FOR SHIFTING	Gammel, Berndt; Klug, Franz; Kniffler, Oliver
10/893,163	7/16/2004	CALCULATING UNIT AND METHOD FOR PERFORMING AN ARITHMETIC OPERATION WITH ENCRYPTED OPERANDS	Gammel, Berndt; Klug, Franz; Kniffler, Oliver
10/896,366	7/21/2004	METHOD AND APPARATUS FOR INTEGRATION OF ELECTRONICS IN TEXTILES	Jung, Stefan; Lauterbach, Christl
10/899,444	7/26/2004	ADHESIONS OF STRUCTURES FORMED FROM MATERIALS OF POOR ADHESION	Ruttkowski, Eike; Hofmann, Franz; Eversmann, Bjorn-Oliver
10/902,486	7/29/2004	IC CHIP WITH NANOWIRES	Mio, Hannes; Kreupl, Franz
10/911,224	8/4/2004	COMPUTER SYSTEM FOR DATA PROCESSING AND METHOD FOR THE TRANSFER OF AN ARRAY SEGMENT OF AN AFFINE-INDEXED MULTI-DIMENSIONAL ARRAY REFERENCED IN A LOOP NEST FROM A FIRST MEMORY TO A SECOND MEMORY	Richter, Mathias; Sauermann, Mirko
10/913,707	8/5/2004	MEMORY CELL	Deppe, Joachim; Kleint, Christoph; Ludwig, Christoph
10/913,956	8/6/2004	KEY MANAGEMENT DEVICE AND METHOD FOR THE ENCRYPTED STORAGE OF DIGITAL DATA WORDS	Gammel, Berndt M.; Kunemund, Thomas; Sedlak, Holger
10/915,258	8/9/2004	USB-BASED PERIPHERAL DEVICE AND METHOD FOR STARTING UP THE USB-BASED PERIPHERAL DEVICE	Gotze, Ralf; Schneckenburger, Christian; Cinkler, Kalman
10/916,308	8/11/2004	ELECTRODEPOSITION DEVICE AND ELECTRODEPOSITION SYSTEM FOR COATING STRUCTURES WHICH HAVE ALREADY BEEN MADE	Muller-Hipper, Andreas;

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		CONDUCTIVE	Simmerlein-Erlbacher, Ewald; Karl, Andreas
10/917,262	8/12/2004	DEVICE AND METHOD FOR CALCULATING A RESULT OF A DIVISION	Fischer, Wieland
10/917,554	8/12/2004	PROCESS FOR PRODUCING A STRUCTURED METAL LAYER ON A SUBSTRATE BODY, AND SUBSTRATE BODY HAVING A STRUCTURED METAL LAYER	Gundlach, Harald; Muller-Hipper, Andreas; Simmerlein-Erlbacher, Ewald
10/920,043	8/16/2004	PROCESS FOR PRODUCING A MULTILAYER ARRANGEMENT HAVING A METAL LAYER	Ruttkowski, Eike; Ilicali, Gurkan; Luyken, R. Johannes; Hofmann, Franz; Alba, Manuela
10/920,150	8/17/2004	CONTROL SYSTEM AND METHOD FOR OPERATING A TRANSCEIVER	Stromberg, Guido; Sturm, Thomas
10/922,791	8/19/2004	METHOD AND APPARATUS FOR COMPUTER-AIDED CREATION OF A CLOCK TREE STRUCTURE FILE, A METHOD FOR COMPUTER-AIDED CREATION OF A LAYOUT FOR A SEMICONDUCTOR CIRCUIT, AND A COMPUTER-READABLE STORAGE MEDIA	Endres, Heinz; Zettler, Thomas
10/923,198	8/20/2004	CIRCUIT ARRANGEMENT HAVING A TRANSMITTER AND A RECEIVER	
10/925,882	8/23/2004	DATA CARRIER CARD	Dirscherl, Gerd; Janssen, Norbert
10/925,903	8/23/2004	PSEUDORANDOM NUMBER GENERATOR	Fischer, Jurgen; Palm, Herbert; Puschner, Frank; Willer, Josef
10/926,838	8/25/2004	FLOATING GATE MEMORY CELL, FLOATING GATE MEMORY ARRANGEMENT, CIRCUIT ARRANGEMENT AND METHOD FOR FABRICATING A FLOATING GATE MEMORY CELL	Dirscherl, Gerd; Gammel, Berndt; Gottfert, Rainer
10/928,646	8/27/2004	METHOD FOR COMPARISON A TEST FINGERPRINT, WHICH IS IN THE FORM OF AN IMAGE STRIP SEQUENCE, WITH A STORED REFERENCE FINGERPRINT, AND A SUITABLE APPARATUS FOR CARRYING OUT THE METHOD	Graham, Andrew; Hoffmann, Franz
10/931,482	9/1/2004	CMOS-PROCESS COMPATIBLE HIGH-DC SURFACE COATING FOR CAPACITIVE DETECTION AND STIMULATION OF BIOLOGICAL TISSUES	Morguet, Peter
10/934,302	9/3/2004	APPARATUS FOR PROVIDING A JITTERED CLOCK SIGNAL AND APPARATUS FOR PROVIDING A RANDOM BIT	Birkenmaier, Tamara; Eversmann, Bjorn-Oliver; Fromherz, Peter; Gabl, Reinhard; Jenker, Marin; Schreiter, Matthias; Thewes, Roland; Wersing, Wolfram
10/937,745	9/8/2004	CAPACITOR ASSEMBLY AND METHOD FOR PRODUCING THE SAME	Luzzi, Raimondo
10/946,579	9/20/2004	VOLTAGE REGULATOR ARRANGEMENT	Schrenk, Michael
10/948,627	9/23/2004	RANDOM NUMBER GENERATOR AND METHOD FOR GENERATING A RANDOM NUMBER	Fleischmann, Peter; Nebel, Gerhard; Schlaffer, Andreas; Weder, Uwe
10/948,637	9/23/2004	METHOD FOR PRODUCING AN SOI FIELD EFFECT TRANSISTOR AND CORRESPONDING FIELD EFFECT TRANSISTOR	Sturm, Thomas; Stromberg, Guido; Brederlow, Ralf; Weber, Werner
10/952,240	9/28/2004	INTEGRATED CIRCUIT HAVING RADIATION SENSOR ARRANGEMENT	Gottsche, Ralf; Pacha, Christian; Schulz, Thomas; Steinhogl, Werner
10/952,472	9/27/2004	LINE-CONDUCTED INTERFACE ARRANGEMENT AND A METHOD FOR OPERATING A LINE-CONDUCTED INTERFACE ARRANGEMENT BETWEEN A HOST AND A DEVICE	Laackmann, Peter; Ruping, Stefan; Cinkler, Kalman

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10/958,182	10/4/2004	CIRCUIT WITH A BUS WITH SEVERAL RECEIVERS	Klug, Franz; Kunemund, Thomas; Sonnekalb, Steffen
10/958,644	10/5/2004	PSEUDORANDOM NUMBER GENERATOR FOR A STREAM CIPHER	Dirscherl, Gerd; Gammel, Berndt; Gottfert, Rainer
10/964,560	10/12/2004	EVENT DETECTION - APPARATUS AND METHOD FOR MEASURING THE ACTIVITY OF NEURAL NETWORKS	Eversmann, Bjorn-Oliver; Jenkner, Martin; Paulus, Christian; Thewes, Roland
10/965,663	10/14/2004	CIRCUIT ARRANGEMENT AND METHOD FOR PRODUCING A DUAL-RAIL SIGNAL	Kunemund, Thomas; Sedlak, Holger
10/966,847	10/15/2004	METHOD FOR REPLACING CONTENTS OF A DATA STORAGE UNIT	Gammel, Berndt
10/967,090	10/15/2004	DEVICE BASED ON PARTIALLY OXIDIZED POROUS SILICON AND METHOD FOR ITS PRODUCTION	Dertinger, Stephan; Fritz, Michaela; Fuchs, Karin; Haneder, Thomas; Lehmann, Volker; Martin, Alfred; Marz, Reinhard
10/968,846	10/18/2004	SUBSTRATE AND METHOD FOR PRODUCING A SUBSTRATE	Hofmann, Franz; Luyken, R. Johannes; Stadele, Martin; Rosner, Wolfgang; Specht, Michael
10/968,847	10/19/2004	DEVICE AND METHOD FOR DETECTING BIOCHEMICAL REACTIONS AND/OR BINDINGS	Dertinger, Stephan; Fritz, Michaela; Fuchs, Karin; Haneder, Thomas; Hanke, Hans-Christian; Martin, Alfred; Marz, Reinhard
10/970,363	10/21/2004	CIRCUIT ARRANGEMENT FOR VOLTAGE REGULATION	Schlaffer, Andreas
10/974,542	10/27/2004	COMPONENT	Hubner, Holger
10/977,561	10/28/2004	APPARATUS AND METHOD FOR CALCULATING A RESULT OF A MODULAR MULTIPLICATION	Fischer, Wieland; Sedlak, Holger; Seifert, Jean-Pierre
10/978,241	10/29/2004	APPARATUS AND METHOD FOR GENERATING A RANDOM NUMBER	Meintrup, David; Stromberg, Guido; Sturm, Thomas; Stohr, Annelie
10/978,242	10/29/2004	INTEGRATED CIRCUIT HAVING A MIXER CIRCUIT	Langenberg, Jorg; Liebermann, Thomas; Simburger, Werner; Tiebout, Marc; Wohlmuth, Hans-Dieter
10/979,787	11/1/2004	GARMENT WITH INTEGRATED ELECTRONIC COMPONENTS	Schweizer, Thomas; Janke, Marcus
10/980,983	11/3/2004	CONTACT-CONNECTION OF NANOTUBES	Unger, Eugen; Dusberg, Georg Stefan; Kreupl, Franz Martin; Graham, Andrew; Liebau, Maik
10/988,422	11/12/2004	CIRCUIT AND METHOD FOR PERFORMING A CALCULATION	Bock, Holger
10/989,600	11/16/2004	CLOCK REGULATION APPARATUS AND CIRCUIT ARRANGEMENT	Mahrla, Peter
10/991,345	11/9/2004	NON-VOLATILE FLASH SEMICONDUCTOR MEMORY AND FABRICATION METHOD	Hofmann, Franz; Landgraf, Erhard;

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10/992,467	11/18/2004	AUTHORIZATION MEANS SECURITY MODULE TERMINAL SYSTEM	Rosner, Wolfgang; Specht, Michael; Staedele, Martin
10/992,627	11/17/2004	APPARATUS FOR HOUSING A MICROMECHANICAL STRUCTURE AND METHOD FOR PRODUCING THE SAME	Hewel, Karl-Harald
10/995,960	11/22/2004	LOGIC BASIC CELL AND LOGIC BASIC CELL ARRANGEMENT	Franosch, Martin; Meckes, Andreas; Nessler, Winfried; Oppermann, Klaus-Gunter
10/996,620	11/19/2004	LOGIC CIRCUIT ARRANGEMENT	Gliese, Jorge; Sauermann, Mirko
10/999,809	11/29/2004	METHOD, COMPUTER PROGRAM, APPARATUS AND SYSTEM FOR THE SELECTIVE COMMUNICATION OF DATA SETS	Gliese, Jorg; Scheppler, Michael
10/999,810	11/29/2004	METHOD FOR THE PRODUCTION OF A MEMORY CELL, MEMORY CELL AND MEMORY CELL ARRANGEMENT	Mergenthaler, Max Egon, Dr.; Preuninger, Jurgen
11/001,781	12/1/2004	MODULE FOR CONTACTLESS CHIP CARDS OR IDENTIFICATION SYSTEMS	Hofmann, Franz; Luyken, Richard Johannes; Specht, Michael
11/006,519	12/6/2004	PROCESSOR AND METHOD FOR A SIMULTANEOUS EXECUTION OF A CALCULATION AND A COPYING PROCESS	Puschner, Frank; Muller-Hipper, Andreas; Karl, Andreas
11/007,591	12/8/2004	PROTECTION OF CHIPS AGAINST ATTACKS	Elbe, Astrid; Fischer, Wieland; Janssen, Norbert; Sedlak, Holger; Seifert, Jean-Pierre
11/007,592	12/8/2004	AUTHENTICITY TAG AND METHOD FOR OPERATING AN AUTHENTICITY TAG	Laackmann, Peter
11/007,650	12/7/2004	LOGIC BASIC CELL, LOGIC BASIC CELL ARRANGMENT AND LOGIC DEVICE	Laackmann, Peter
11/008,585	12/9/2004	DEVICE AND METHOD FOR GENERATING RANDOM NUMBERS USING A PSEUDO RANDOM NUMBER GENERATOR	Gliese, Jorge; Schonauer, Tim
11/009,402	12/10/2004	MICROPROCESSOR ARRANGEMENT AND METHOD FOR OPERATING A MICROPROCESSOR ARRANGEMENT	Gammel, Berndt; Goettfert, Rainer; Sedlak, Holger
11/016,374	12/17/2004	SYSTEM FOR INTERCHANGING DATA BETWEEN AT LEAST TWO CONTACTLESS DATA STORAGE MEDIA	Janke, Marcus
11/018,404	12/20/2004	LOGIC CIRCUIT	Berger, Dominik; Melchior, Marc
11/019,328	12/14/2004	DEVICE FOR OPTICAL SIGNAL TRANSMISSION, METHOD FOR OPTICAL SIGNAL TRANSMISSION AND OPTICAL MODULATOR	Bock, Holger
11/020,631	12/22/2004	DEVICE AND METHOD FOR PERFORMING A CRYPTOALGORITHM	Averbeck, Robert; Stegmuller, Bernhard
11/022,278	12/23/2004	TRANSMISSION DEVICE	Rueping, Stephan
11/035,765	1/13/2005	SENSOR ARRANGEMENT WITH IMPROVED SPATIAL AND TEMPORAL RESOLUTION	Kunemund, Thomas
11/041,084	1/21/2005	SEMICONDUCTOR MEMORY DEVICE AND CIRCUIT ARRANGEMENT	Eversmann, Bjorn-Oliver; Jenkner, Martin; Paulus, Christian; Stromberg, Guido; Sturm, Thomas; Stohr, Annelie
11/045,002	1/26/2005	INTEGRATED CIRCUIT ARRANGEMENT	Dortu, Jean-Marc
11/053,096	2/7/2005	VARIABLE LENGTH INSTRUCTION PIPELINE	Janke, Marcus; Laackmann, Peter
11/055,570	2/9/2005	CONTACTLESS DATA STORAGE MEDIUM HAVING A SIGNAL INDICATOR	Norden, Erik K.; Arnold, Roger D.; Ober, Robert E.; Hastie, Neil S.
11/062,317	2/18/2005	METHOD FOR OPERATING A PERIPHERAL DEVICE ON A BUS SYSTEM	Ruping, Stefan; Klement, Rupert; Schneckenburger,

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		OF A COMPUTER SYSTEM	Christian
11/062,318	2/18/2005	PROCESS FOR PRODUCING A METAL STRUCTURE IN FOAM FORM, A METAL FOAM, AND AN ARRANGEMENT HAVING A CARRIER SUBSTRATE AND A METAL FOAM	Karl, Andreas; Muller-Hipper, Andreas; Simmerlein-Erlbacher, Ewald
11/062,319	2/18/2005	READING DEVICE AND METHOD FOR CHECKING THE FUNCTIONALITY OF A READING DEVICE OF THIS TYPE	Berger, Dominik
11/064,525	2/22/2005	METHOD AND CIRCUIT FOR CONTROLLING THE INPUT SIGNAL FOR A CONTACTLESS TRANSPONDER	Alihodzic, Admir; Kargl, Walter; Missoni, Albert
11/064,528	2/22/2005	METHOD FOR SMOOTHING AREAS IN STRUCTURES BY UTILIZING THE SURFACE TENSION	Domke, Wolf-Dieter; Schwarzl, Siegfried
11/067,309	2/25/2005	DECRYPTION CIRCUIT, ENCRYPTION CIRCUIT, LOGIC CELL, AND METHOD OF PERFORMING A DUAL-RAIL LOGIC OPERATION IN SINGLE-RAIL LOGIC ENVIRONMENT	Kunemund, Thomas
11/070,843	2/24/2005	DATA PROCESSING APPARATUS HAVING PROGRAM COUNTER SENSOR	Gail, Markus; Otterstedt, Jan
11/073,017	3/4/2005	HIGH-DENSITY NROM-FINFET	Hofmann, Franz; Landgraf, Erhard; Luyken, Richard Johannes; Rosner, Wolfgang; Specht, Michael
11/073,018	3/4/2005	KEY BIT STREAM GENERATION	Gottfert, Rainer; Gammel, Berndt; Cinkler, Kalman; Ruping, Stefan
11/080,998	3/14/2005	CALCULATING UNIT	Janssen, Norbert; Roemer, Tanja; Sedlak, Holger
11/083,334	3/17/2005	RECTIFIER CIRCUIT	Alihodzic, Admir
11/084,216	3/18/2005	METHOD AND CIRCUIT ARRANGEMENT FOR DEMODULATING A DIGITAL AMPLITUDE-MODULATED RADIO SIGNAL	Alihodzi, Admir
11/084,725	3/18/2005	RANDOM NUMBER GENERATOR AND METHOD FOR GENERATING RANDOM NUMBERS	Gammel, Berndt; Goettfert, Rainer
11/086,801	3/22/2005	METHOD AND SYSTEM FOR COMPUTER-AIDED SPEECH SYNTHESIS	Küstner, Michael; Schnell, Markus
11/088,228	3/23/2005	CONTACTLESS DATA CARRIER WITH CURRENT DEMODULATION	Missoni, Albert
11/090,914	3/24/2005	APPARATUS AND METHOD FOR CONVERTING, AND ADDER CIRCUIT	Elbe, Astrid; Janssen, Norbert; Sedlak, Holger
11/090,972	3/24/2005	CONTACTLESS DATA STORAGE MEDIUM AND METHOD FOR OPERATING CONTACTLESS DATA STORAGE MEDIUM	Berger, Dominik
11/093,276	3/28/2005	PROCESSOR FOR PROCESSING A PROGRAM	Dirscherl, Gerd; Gammel, Berndt; Smola, Michael
11/094,841	3/30/2005	TRANSMISSION INTERFACE	Ruping, Stefan; Sustek, Laurent
11/097,968	3/31/2005	METHOD AND SYSTEM FOR CONTACTLESS DATA TRANSMISSION	Kargl, Walter
11/101,171	4/7/2005	METHOD AND APPARATUS FOR CORRELATING CODES IN CDMA SYSTEMS	Medlock, Joel D.
11/101,365	4/7/2005	METHOD AND APPARATUS FOR CONFIGURING INFORMATION	Cummings, Mark R.
11/104,293	4/11/2005	CIRCUIT ARRANGEMENT	Missoni, Albert
11/107,986	4/15/2005	CIRCUIT ARRANGEMENT FOR VOLTAGE REGULATION	Baglin, Thomas Jean Ludovic; Nebel, Gerhard
11/111,096	4/19/2005	METHOD AND DEVICE FOR DETERMINING A RESULT	Sonnekalb, Steffen Marc; Wenzel, Andreas
11/112,739	4/22/2005	METHOD FOR PRODUCING A BGA CHIP MODULE AND BGA CHIP MODULE	Puschner, Frank; Schindler, Wolfgang; Tutsch, Gunter

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11/115,985	4/27/2005	DATA PROCESSING APPARATUS AND METHOD FOR OPERATING A DUAL RAIL CIRCUIT	Kunemund, Thomas
11/116,139	4/27/2005	INTEGRATED CIRCUIT ARRAY	Graham, Andrew; Hofmann, Franz; Honlein, Wolfgang; Kretz, Johannes; Kreupl, Franz; Landgraf, Erhard; Luyken, Richard Johannes; Rosner, Wolfgang; Schulz, Thomas; Specht, Michael
11/119,426	4/28/2005	COMMUNICATIONS SYSTEM	Dirscherl, Gerd; Winteler, Till
11/119,531	4/29/2005	MEMORY CELL, MEMORY CELL ARRANGEMENT, PATTERNING ARRANGEMENT, AND METHOD FOR FABRICATING A MEMORY CELL	Graham, Andrew; Hofmann, Franz; Honlein, Wolfgang; Kretz, Johannes; Kreupl, Franz; Landgraf, Erhard; Luyken, Richard Johannes; Rosner, Wolfgang; Schulz, Thomas; Specht, Michael
11/120,659	5/2/2005	DEVICE AND METHOD FOR GENERATING A PSEUDORANDOM SEQUENCE OF NUMBERS	Goettfert, Rainer; Meyer, Bernd; Dirscherl, Gerd; Seifert, Jean-Pierre
11/120,660	5/2/2005	APPARATUS AND METHOD FOR STRUCTURE EXPOSURE OF A PHOTOREACTIVE LAYER	Stommer, Ralph
11/121,689	5/3/2005	SMART CARD	Knoll, Bernhard Theo
11/125,819	5/9/2005	MEMORY ARRANGEMENT	Dirscherl, Gerd; Peters, Christian; Sedlak, Holger
11/129,068	5/13/2005	DEVICE AND METHOD FOR SUPPLYING A DATA TRANSFER UNIT WITH ENERGY	Baglin, Thomas Jean Ludovic; Missoni, Albert
11/130,555	5/16/2005	CONTROLLING OUT OF ORDER EXECUTION PIPELINES ISSUE TAGGING	Hastie, Neil S.
11/131,452	5/16/2005	LOGIC BASIC CELL	
11/132,145	5/20/2005	CONTROLLER HAVING DECODING MEANS	Gliese, Jorg
11/134,026	5/19/2005	INTERFACE CIRCUIT	Klug, Franz; Kniffler, Oliver; Sonnekalb, Steffen; Wenzel, Andreas
11/134,612	5/20/2005	CONTROLLER AND METHOD FOR PROCESSING INSTRUCTIONS	Beit-Grogger, Andre; Koroschetz, Helmut
11/136,611	5/23/2005	METHOD FOR DETECTING THE RELATIVE MOVEMENT OF A FINGER IN RELATION TO A SENSOR SURFACE	Klug, Franz; Kniffler, Oliver; Sonnekalb, Steffen; Wenzel, Andreas
11/137,698	5/24/2005	METHOD AND SYSTEM FOR PRODUCING CONDUCTIVE PATTERNS ON A SUBSTRATE	Lorch, Henning; Morguet, Peter
11/138,909	5/25/2005	METHOD AND SYSTEM FOR CHECKING COMPLETENESS IN A PACKAGE	Karl, Andreas; Muller-Hipper, Andreas; Puschner, Frank; Simmerlein-Erlbacher, Ewald
11/142,007	5/31/2005	COMMUNICATION SYSTEM	Berger, Dominik; Muller, Dietmar; Weitlaner, Matthias; Schmidt, Holger; Hans, Martin

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11/144,373	6/3/2005	ANALOG DC COMPENSATION	Beckmann, Mark
11/147,401	6/6/2005	COMMUNICATION SYSTEM	Hjelm, Mikael
			Junfeng, Rong; Eckert, Michael; Wuschke, Martin; Schmidt, Holger
11/147,414	6/6/2005	CONFIGURABLE LOGIC CIRCUIT	Otterstedt, Jan
11/147,418	6/6/2005	RADIO COMMUNICATION RECEIVER	Rudberg, Mikael
11/149,450	6/8/2005	FREQUENCY CONVERTER CIRCUIT	Knapp, Herbert; Wurzer, Martin
11/152,420	6/13/2005	METHOD AND SYSTEM FOR CONTACTLESS DATA TRANSMISSION, AND USE OF THE METHOD	Berger, Dominik
11/153,914	6/14/2005	METHOD AND SYSTEM FOR OPERATING A CACHE MEMORY	Dirscherl, Gerd; Gammel, Berndt M.; Smola, Michael
11/155,321	6/16/2005	CIRCUIT FOR GENERATING A SUPPLY VOLTAGE	Alihodzic, Admir; Baglin, Thomas Jean Ludovic
11/155,322	6/16/2005	TRANSISTOR ARRANGEMENT	Brederlow, Ralf
11/157,495	6/20/2005	RADIO-INTERROGABLE DATA STORAGE MEDIUM	Laackmann, Peter
11/157,496	6/20/2005	FIN FIELD EFFECT TRANSISTOR MEMORY CELL	Dreeskornfeld, Lars; Hartwich, Jessica; Hofmann, Franz; Kretz, Johannes; Specht, Michael
11/165,823	6/23/2005	PARALLEL DATA BUS	Smola, Michael; Gammel, Berndt M.; Dirscherl, Gerd
11/165,834	6/23/2005	MODULAR MULTIPLICATION WITH PARALLEL CALCULATION OF THE LOOK-AHEAD PARAMETERS	Fischer, Wieland; Sedlak, Holger; Seifert, Jean-Pierre
11/166,645	6/23/2005	APPARATUS AND METHOD FOR CALCULATING A MULTIPLICATION	Fischer, Wieland; Seifert, Jean-Pierre; Sedlak, Holger
11/166,657	6/23/2005	METHOD AND DEVICE FOR EXTRACTING A CLOCK FREQUENCY UNDERLYING A DATA STREAM	Greco, Patrizio; Steinschaden, Andreas; Thaller, Edwin; Zessar, Gernot
11/166,685	6/23/2005	DIGITALLY CONTROLLABLE OSCILLATOR	Greco, Patrizia; Steinschaden, Andreas; Thaller, Edwin; Zessar, Gernot
11/169,157	6/27/2005	APPARATUS FOR DETECTING CONNECTION OF A PERIPHERAL UNIT TO A HOST SYSTEM	Cinkler, Kalman; Winteler, Till
11/175,912	7/5/2005	LAYER ARRANGEMENT AND PROCESS FOR PRODUCING A LAYER ARRANGEMENT	Ilicali, Gurkan; Landgraf, Erhard; Roesner, Wolfgang; Hofmann, Franz
11/176,587	7/6/2005	SELECTION CIRCUIT	Baglin, Thomas Jean Ludovic
11/176,941	7/6/2005	DATA STORAGE MEDIUM HAVING A TEST MODE	Papadopoulos, Constantin; Gammel, Berndt M.
11/177,235	7/7/2005	PLANAR DUAL-GATE TRANSISTOR AND METHOD FOR FABRICATING A PLANAR DUAL-GATE TRANSISTOR	Ilicali, Gurkan; Luyken, Richard Johannes; Rosner, Wolfgang
11/178,251	7/8/2005	PROCESS FOR PRODUCING A LAYER ARRANGEMENT, AND LAYER ARRANGEMENT FOR USE AS A DUAL-GATE FIELD-EFFECT TRANSISTOR	Luyken, Richard Johannese; Ilicali, Gurkan; Johannese, Richard; Rosner, Wolfgang

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11/179,356	7/11/2005	APPARATUS AND METHOD FOR MULTIMODE DIGITAL DESPREADER FOR CDMA SIGNAL PROCESSING	Subramanian, Ravi
11/180,039	7/11/2005	SEMICONDUCTOR CHIP STACK	Hubner, Holger
11/181,032	7/12/2005	VOLTAGE SUPPLY CIRCUIT AND METHOD FOR GENERATING A SUPPLY VOLTAGE	Baglin, Thomas Jean Ludovic; Missoni, Albert; Nebel, Gerhard
11/181,033	7/12/2005	INTEGRATED CIRCUIT	Kunemund, Thomas
11/184,566	7/18/2005	METHOD AND APPARATUS FOR DETECTING LUMINESCENCE LIGHT FROM A POROUS SUPPORT STRUCTURE	Dertinger, Stephan; Kluehr, Marco
11/187,039	7/20/2005	SECURE MASKING IN THE PRESENCE OF GLITCHES	Gammel, Berndt; Fischer, Wieland
11/193,038	7/29/2005	DEVICE AND METHOD FOR CALCULATING ENCRYPTED DATA FROM UNENCRYPTED DATA OR UNENCRYPTED	Bock, Holger; Aigner, Manfred
11/193,851	7/29/2005	A WIRELESS SPREAD SPECTRUM COMMUNICATION PLATFORM USING DYNAMICALLY RECONFIGURABLE LOGIC	Subramanian, Ravi; Jha, Uma; Medlock, Joel D.
11/195,332	8/1/2005	LAYER ARRANGEMENT	Schindler, Gunther; Pamler, Werner
11/195,350	8/1/2005	MODULAR EXPONENTIATION WITH RANDOMIZED EXPONENT	Fischer, Wieland
11/197,776	8/3/2005	DEVICE AND METHOD FOR GENERATING A SEQUENCE OF NUMBERS	Gottfert, Rainer; Gammel, Berndt
11/198,692	8/5/2005	A WIRELESS SPREAD SPECTRUM COMMUNICATION PLATFORM USING DYNAMICALLY RECONFIGURABLE LOGIC	Subramanian, Ravi; Jha, Uma; Medlock, Joel D.
11/200,854	8/9/2005	METHOD AND APPARATUS TO SUPPORT MULTISTANDARD, MULTISERVICE BASE-STATIONS FOR WIRELESS VOICE AND DATA NETWORKS	Subramanian, Ravi; Rieken, Keith; Woodthrope, Christopher C.
11/202,944	8/12/2005	INTEGRATED CIRCUIT	Gammel, Berndt; Kunemund, Thomas
11/207,599	8/19/2005	TEXTILE FABRIC STRUCTURE	Glaser, Rupert Hermann Josef; Jung, Stefan; Lauterbach, Christ
11/211,167	8/23/2005	CHIP CARD MODULE	Puschner, Frank; Stampka, Peter; Muller-Hipper, Andreas; Schindler, Wolfgang
11/211,168	8/23/2005	INTEGRATED ELECTRONIC COMPONENT	Seidel, Robert; Kreupl, Franz; Graham, Andrew
11/213,341	8/25/2005	SEMICONDUCTOR CHIP ARRANGEMENT	Eigner, Markus; Gruber, Wolfgang; Roth, Manfred; Ruping, Stefan
11/213,663	8/25/2005	CIRCUIT ARRANGEMENT	Ruping, Stefan
11/215,350	8/29/2005	COMMUNICATION SYSTEM	Hans, Martin; Luft, Achim
11/216,457	8/30/2005	CHIP MODULE	Muller-Hipper, Andreas; Puschner, Frank
11/218,315	8/31/2005	ENHANCED MULTI-MEDIA-CARD AND CARD-READER FOR EMERGENCE RELATED APPLICATIONS IN MOBILE	Kodim, Walter
11/221,382	9/6/2005	METHOD FOR OPERATING A DATA STORAGE MEDIUM	Ruping, Stefan
11/221,383	9/6/2005	METHOD FOR OPERATING A MICROPROCESSOR	Gammel, Berndt; Sonnekalb, Steffen
11/222,504	9/7/2005	APPARATUS AND METHOD FOR RECOGNIZING A FAILURE OF A CRYPTOGRAPHIC UNIT	Gammel, Berndt; Goessel, Michael; Goettfert, Rainer
11/227,963	9/14/2005	MEDIUM ACCESS CONTROL UNIT	Choi, Hyung-Nam; Eckert, Michael

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11/227,964	9/14/2005	CHIP CARD	
11/233,419	9/16/2005	APPARATUS AND METHOD FOR GENERATING A FIBONACCI VECTOR FIELD FROM A GALOIS FIELD	Kargl, Walter; Medlock, Joel D.
11/236,311	9/26/2005	METHOD FOR PRODUCING CHIP STACKS	
11/238,645	9/28/2005	DATA LINK LAYER PROTOCOL UNIT	Hubner, Holger
11/240,815	9/30/2005	ARRANGEMENT AND METHOD FOR CONTROLLING COMMUNICATION OF DATA BETWEEN PROCESSORS	Eckert, Michael; Choi, Hyung-Nam; Wuschke, Martin
11/240,816	9/30/2005	MEMORY ARRANGEMENT	Esch, Michael; Rost, Astrid
11/240,817	9/30/2005	VOLTAGE SUPPLY DEVICE AND METHOD FOR OPERATING SUCH A VOLTAGE SUPPLY DEVICE	Scheiblhofer, Dietmar; Jernej, Jurgen
11/246,015	10/6/2005	RANDOM NUMBER GENERATOR AND METHOD FOR GENERATING RANDOM	Scheiblhofer, Dietmar
11/251,623	10/14/2005	SHELF ARRANGEMENT AND SHELF UNIT	Luzzi, Raimondo; Bucci, Marco
11/252,361	10/13/2005	APPARATUS AND METHOD FOR REQUESTING OR ALLOCATING A PUSH-TO-TALK RIGHT TO TALK AND/OR FOR REQUESTING OR COMMUNICATING QUEUING INFORMATION	Glaser, Rupert; Lauterbach, Christl; Sturm, Thomas
11/261,453	10/27/2005	DEVICE AND METHOD FOR THE COMPUTER-AIDED MANAGEMENT OF A TELECOMMUNICATION CONFERENCE	Schwagmann, Norbert; Schmidt, Andreas; Schmidt, Holger
11/264,294	10/31/2005	INTEGRATED CIRCUIT ARRANGEMENT	Schmidt, Holger; Schwagmann, Norbert; Schmidt, Andreas
11/264,679	10/31/2005	METHOD FOR AUTOMATICALLY SETTING UP AND/OR CONTROLLING A TELECOMMUNICATION CONFERENCE	Benetik, Thomas; Hodel, Uwe; Kienmayer, Christoph; Streibl, Martin; Tiebout, Marc
11/264,703	10/31/2005	CIRCUIT ARRANGEMENT HAVING SECURITY AND POWER SAVING MODES	Schwagmann, Norbert; Schmidt, Andreas; Schmidt, Holger; Benthin, Marcus, Dr.
11/267,027	11/3/2005	DEVICE AND METHOD FOR TREATING A STATE OF A MEMORY	Kunemund, Thomas
11/271,157	11/11/2005	SYSTEM AND METHOD FOR PROCESSING DIGITAL MEDIA CONTENT IN A MOBILE DEVICE	Fischer, Wieland; Samec, Christian
11/280,937	11/15/2005	COMPUTER DEVICE	van Niekert, Sabine; Schmidt, Andreas
11/283,539	11/18/2005	IDENTIFICATION SYSTEM FOR INDENTIFYING OBJECTS	Mielenz, Carsten; Gruber, Hans-Georg
11/288,961	11/28/2005	METHOD AND SYSTEM FOR CARRYING OUT A PROCESS ON AN INTEGRATED CIRCUIT	Kienzl, Katja; Weitlaner, Matthias
11/288,990	11/28/2005	CHIP CARD MODULE	Grebner, Thomas
11/291,097	11/29/2005	DEVICE AND METHOD FOR PROCESSING A PROGRAM CODE	Latka, Bettina; Puschner, Frank; Schindler, Wolfgang; Stampka, Peter
11/299,278	12/8/2005	PRESENCE SERVER UNIT	Bruecklmayr, Franz-Josef; Lippmann, Bernhard
11/305,821	12/16/2005	CIRCUIT ARRANGEMENT	Schmidt, Andreas; Schmidt, Holger
			Haider, Gunter; Nebel, Gerhard; San Sebastian, Iker;

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			Sedlak, Holger; Weder, Uwe
11/313,241	12/20/2005	APPARATUS AND METHOD FOR DETECTING A POTENTIAL ATTACK ON A CRYPTOGRAPHIC CALCULATION	Fischer, Wieland
11/317,220	12/22/2005	APPARATUS AND METHOD FOR CONFIGURABLE ALL-DIGIT COHERENT DEMODULATOR SYSTEM FOR WCDMA APPLICATIONS	Subramanian, Ravi
11/317,524	12/22/2005	APPARATUS AND METHOD FOR CONFIGURABLE ALL-DIGIT COHERENT DEMODULATOR SYSTEM FOR WCDMA APPLICATIONS	Subramanian, Ravi
11/317,640	12/23/2005	DATA PROCESSING DEVICE	Delfs, Eckhard; Hildebrand, Uwe; Jennings, David; Goedecke, Michael
11/318,061	12/23/2005	CRYPTOGRAPHIC UNIT AND METHOD FOR OPERATING A CRYPTOGRAPHIC UNIT	Gammel, Berndt; Bock, Holger; Gossel, Michael
11/323,470	12/29/2005	METHOD FOR COMPUTER-AIDED MANAGEMENT OF COMMUNICATION RIGHTS FOR COMMUNICATION BY MEANS OF PLURALITY OF DIFFERENT COMMUNICATION MEDIA IN A COMMUNICATION CONFERENCE HAVING A PLURALITY OF TELECOMMUN	Hans, Martin; Schmidt, Holger; Schwagmann, Norbert
11/329,363	1/9/2006	INTEGRATED CIRCUIT	
11/332,779	1/12/2006	PHOTOLITHOGRAPHY ARRANGEMENT	Spirkl, Wolfgang; Schmid, Gunter
11/333,552	1/16/2006	METHOD AND CIRCUIT ARRANGEMENT FOR VERIFYING A DATA RECORD HAVING A PLURALITY OF DATA	Marz, Reinhard
11/335,158	1/18/2006	VOLTAGE REGULATOR	Sonnekalb, Steffen
			Haider, Gunter; Nebel, Gerhard; San Sebastian, Iker; Schlaffer, Andreas; Weder, Uwe
11/337,078	1/19/2006	COMMUNICATION SYSTEM HAVING CONFERENCE SERVER	Hans, Martin; Schwagmann, Norbert
11/340,000	1/25/2006	CIRCUIT ARRANGEMENT FOR VOLTAGE ADJUSTMENT	Haider, Gunter; Mayerl, Christoph; Nebel, Gerhard; San Sebastian, Iker; Sedlak, Holger; Weder, Uwe
11/346,582	2/1/2006	DATA LINK LAYER PROTOCOL UNIT	Choi, Hyung-Nam; Kahlstatt, Harald
11/346,997	2/3/2006	MOBILE RADIO COMMUNICATION SYSTEM	Gruber, Roland
11/351,322	2/8/2006	PROTECTED CHIP STACK	Hubner, Holger; Gammel, Berndt M.
11/355,907	2/15/2006	CIRCUIT ARRANGEMENT AND METHOD FOR SECURE DATA PROCESSING	Klug, Franz; Sonnekalb, Steffen
11/357,714	2/17/2006	PROCESSOR HAVING ELECTRONIC FUSES FOR STORING SECRET DATA	Fischer, Wieland; Seifert, Jean-Pierre
11/357,731	2/17/2006	MACROPOROUS SUPPORT FOR CHEMICAL AMPLIFICATION REACTIONS	Dertinger, Stephan; Kluehr, Marco; Haneder, Thomas
11/359,870	2/21/2006	SEMICONDUCTOR DEVICE WITH DIAGONAL VIAS	Grimm, Wolfgang
11/361,322	2/23/2006	XOR-CIRCUIT	Roemer, Tanja; Janssen, Norbert
11/361,708	2/24/2006	MINIATURIZED DETECTION COIL FORMER FOR NMR SPECTROSCOPY	Martin, Alfred
11/368,016	3/2/2006	DEVICE AND METHOD FOR A SECURE EXECUTION OF A PROGRAM	Amschler, Harald; Gammel, Berndt
11/368,275	3/3/2006	COMMUNICATION SYSTEM	Eckert, Michael; Choi, Hyung-Nam; Benkert, Michael; Bienas, Maik
11/370,192	3/6/2006	PROCESSOR CIRCUIT AND METHOD OF ALLOCATING A LOGIC CHIP	Fischer, Wieland;

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		TO A MEMORY CHIP	Seifert, Jean-Pierre
11/370,199	3/6/2006	TREE-BASED BROADCASTING SERVICE OVER PUSH-TO- TALK MOBILE IP NETWORK	Jagannathan, Sankarnarayan; Gupta, Vishal
11/370,205	3/6/2006	METHOD FOR DIFFUSION SOLDERING	Hubner, Holger
11/373,829	3/10/2006	SECURE AND FAST CALCULATING UNIT	Janssen, Norbert; Roemer, Tanja
11/388,327	3/23/2006	SEMICONDUCTOR DEVICE AND METHOD FOR PRODUCING A SEMICONDUCTOR DEVICE	Puschner, Frank; Heinemann, Erik; Janka, Stephan
11/390,546	3/24/2006	DEVICE AND METHOD FOR TRANSMITTING DATA IN A ENCRYPTED MANNER	Gammel, Berndt; Scheiblhofer, Dietmar
11/391,666	3/27/2006	CIRCUIT ARRANGEMENT AND METHOD FOR PROCESSING A DUAL-RAIL SIGNAL	Roth, Manfred
60/018,103	6/10/1996	METHOD AND APPARATUS FOR COMPILING ONE CIRCUIT IN A SEQUENCE OF CIRCUITS WITHIN A PROGRAMMABLE GATE ARRAY	Cummings, Mark R.; Watson, John L.
60/022,089	6/10/1996	METHOD AND APPARATUS FOR CONFIGURING INFORMATION	Cummings, Mark R.
60/133,130	5/7/1999	METHOD OF PROFILING DISPARATE COMMUNICATIONS AND SIGNAL PROCESSING STANDARDS AND SERVICES	Subramanian, Ravi
60/133,131	5/7/1999	PROGRAMMABLE LOGIC DEVICE WITH A PROGRAMMABLE SECURITY CIRCUIT	Wasson, Stephen L.; Varn, David K.; Ralston, John D.
60/133,134	5/7/1999	PROGRAMMABLE DATA ARITHMETIC ARRAYS	Wasson, Stephen L.
60/133,136	5/7/1999	PROGRAMMABLE DIGITAL INTERMEDIATE FREQUENCY TRANSCEIVER	
60/133,138	5/7/1999	APPLICATION AND METHODS FOR DYNAMICALLY DEFINING VARIABLY SIZED AUTONOMOUS SUB-ARRAYS WITHIN A PROGRAMMABLE GATE ARRAY	Wasson, Stephen L.
60/133,141	5/7/1999	REPROGRAMMABLE DIGITAL WIRELESS COMMUNICATION DEVICE AND METHOD OF OPERATING SAME	Subramanian, Ravi
60/133,143	5/7/1999	APPARATUS AND METHOD FOR DESIGNING A CIRCUIT USING MINIMUM SLICE CONSTRUCTION AND REPLICATION	Wasson, Stephen L.
60/133,144	5/7/1999	APPARATUS AND METHOD FOR INTERLEAVING A SIGNAL CARRY CHAIN IN AN INTEGRATED CIRCUIT	Wasson, Stephen L.; Varn, David K.; Ralston, John D.
60/173,629	12/30/1999	A FAST INITIAL ACQUISITION/SEARCH METHODOLOGY FOR CDMA AND W-CDMA SYSTEMS	Medlock, Joel D.
60/173,630	12/30/1999	METHOD AND APPARATUS TO SUPPORT MULTISTANDARD, MULTISERVICE BASE-STATIONS FOR WIRELESS VOICE AND DATA NETWORKS	Subramanian, Ravi; Ricken, Keith; Woodthrope, Christopher C.
60/173,631	12/30/1999	APPARATUS AND METHOD FOR GENERATING A FIBONACCI VECTOR FIELD FROM A GALOIS FIELD	Medlock, Joel D.
60/173,632	12/30/1999	A CONFIGURABLE CODE GENERATOR SYSTEM FOR SPREAD SPECTRUM APPLICATIONS	Medlock, Joel D.; Chou, Paul L.
60/173,633	12/30/1999	APPARATUS AND METHOD FOR CONFIGURABLE ALL-DIGIT COHERENT DEMODULATOR SYSTEM FOR WCDMA APPLICATIONS	Subramanian, Ravi
60/173,634	12/30/1999	APPARATUS AND METHOD FOR MULTIMODE DIGITAL DESPREADER FOR CDMA SIGNAL PROCESSING	Subramanian, Ravi
60/178,813	1/28/2000	METHOD AND APPARATUS FOR CORRELATING CODES IN CDMA SYSTEMS	Medlock, Joel D.
60/178,815	1/28/2000	METHOD AND APPARATUS TO SUPPORT MULTISTANDARD, MULTISERVICE BASE-STATIONS FOR WIRELESS VOICE AND DATA NETWORKS	Subramanian, Ravi; Ricken, Keith; Woodthrope, Christopher C.
60/178,817	1/28/2000	APPARATUS AND METHOD FOR AUGMENTING AN M-SEQUENCE WITH ARBITRARY PHASE SHIFT	Medlock, Joel D.
60/178,828	1/28/2000	A METHOD OF GENERATING A CONFIGURATION FOR A CONFIGURABLE SPREAD SPECTRUM COMMUNICATION DEVICE	Subramanian, Ravi; Woodthorpe, Christopher
60/178,830	1/28/2000	METHOD AND APPARATUS FOR PROCESSING A SECONDARY SYNCHRONIZATION CHANNEL IN A SPREAD SPECTRUM SYSTEM	Jha, Uma

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60/195,096	4/6/2000	VIRTUAL MACHINE INTERFACE FOR HARDWARE RECONFIGURABLE AND SOFTWARE PROGRAMMABLE PROCESSORS	Chen, Song; Hesky, Kenneth M.; Joag, Raju R.; Medlock, Joel D.; Woodthorpe, Christopher C.
60/220,295	7/24/2000	DISTRIBUTED MICRO INSTRUCTION SET PROCESSOR ARCHITECTURE FOR HIGH-EFFICIENCY SIGNAL PROCESSING	Chen, Song; Chou, Paul L.; Woodthrope, Christopher C.; Balasubramonian, Venugopal; Rieken, Keith
60/222,007	7/31/2000	METHOD AND APPARATUS FOR TIME-SLICED AND MULTI-THREADED DATA PROCESSING IN A COMMUNICATION SYSTEM	Rieken, Keith; Medlock, Joel D.; Holmes, David M.
60/222,009	7/31/2000	GENERIC FINGER ARCHITECTURE WCDMA APPLICATIONS	Subramanian, Ravi; Rieken, Keith; Jha, Uma; Holmes, David M.; Medlock, Joel D.; Krishnan, Murali
60/222,022	7/31/2000	METHOD AND APPARATUS FOR CONFIGURABLE MULTI-DWELL SEARCH ENGINE FOR CDMA APPLICATIONS	Rieken, Keith
60/222,027	7/31/2000	METHOD AND APPARATUS FOR SAMPLE SELECTION AND REUSE OF RAKE FINGERS IN SPREAD SPECTRUM SYSTEMS	Medlock, Joel D.; Rieken, Keith; Holmes, David M.
60/222,827	8/3/2000	FLEXIBLE TDMA SYSTEM ARCHITECHTURE	Subramanian, Ravi; Woodthrope, Christopher C.; Holmes, David M.; Jha, Uma
60/222,853	8/3/2000	DYNAMICALLY RECONFIGURABLE TRANSMITTER SYSTEM	Medlock, Joel D.
60/222,855	8/3/2001	FLEXIBLE PREAMBLE PROCESSING	Balasubramonian, Venugopal; Goslin, Gregory R.
60/224,172	8/9/2000	ARCHITECTURE AND PROGRAMMING INTEFACE FOR EFFICIENT SOFTWARE DOWNLOAD	Ralston, John D.; Chen, Song; Subramanian, Ravi; Williams, Ted E.
60/258,865	12/29/2000	CHANNEL CODEC PROCESSOR CONFIGURABLE FOR MULTIPLE WIRELESS COMMUNICATIONS STANDARDS	Bitterlich, Stefan Johannes; Holmes, David Morris
60/310,193	8/2/2001	CONFIGURABLE TERMINAL ENGINE	Gavnoudias, Stratis; Boccuzzi, Joseph; Jacques, Alexander; Misra, Raj; Zhang, Yan; Silverberg, Avi; Rizza, Rich; Nacson, Yosef
60/313,991	8/21/2001	METHOD AND APPARATUS FOR ENHANCING DATA RATES IN SPREAD SPECTRUM COMMUNICATION SYSTEMS	Jalloul, Louay; Shanbhag, Abhijit G.
60/315,995	8/29/2001	INTEGRATED CIRCUIT CHIP DESIGN	Williams, Ted E.; Ferro, Jonathan; Tovey, Deforest; Tseng, Louis
60/322,869	9/12/2001	CDMA WIRELESS SYSTEMS	Naguib, Ayman F.
60/346,143	1/4/2002	APPARATUS AND METHOD FOR FAST HADAMARD TRANSFORMS	Medlock, Joel D.
60/606,259	9/1/2004	ENHANCED MULTI-MEDIA-CARD AND CARD-READER FOR EMERGENCE RELATED APPLICATIONS IN MOBILE PHONES AND MOBILE PLATFORMS	Kodim, Walter
60/657,685	3/1/2005	SECURE MASKING IN THE PRESENCE OF GLITCHES	Gammel, Berndt; Fischer, Wieland
60/739,305	11/23/2005	RADIO COMMUNICATION DEVICE, METHOD FOR CONTROLLING A	van Nickerk,

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		CHIP CARD, RADIO COMMUNICATION TERMINAL, CHIP CARD AND METHOD FOR OPERATING A CHIP CARD	Sabine; Schwagmann, Norbert; Luft, Achim
60/756,877	1/5/2006	LOGIC CIRCUIT AND METHOD FOR CALCULATING A MASKED OUTPUT OPERAND	Degrendel, Antoine; Kamp, Winfried; Roth, Manfred; Kodytek, Thomas
60/756,878	1/5/2006	CIRCUIT AND METHOD FOR CALCULATING A LOGIC TIE BETWEEN TWO ENCRYPTED INPUT OPERANDS	Degrendel, Antoine; Kamp, Winfried; Roth, Manfred
TBD Claims priority to PCT/DE2004/0021 18, filed 9/23/2004 Attorney Docket #: E0196.0099	3/29/2006	CHIPCARD PROVIDED WITH CONTACT REGIONS AND METHOD FOR PRODUCING SAID CONTACT REGIONS	Puschner, Frank; Schindler, Wolfgang; Simmerlein-Erlbacher, Ewald; Stampka, Peter
TBD Claims priority to DE 102005012632.4, filed 3/18/2005 Attorney Docket #: E0196.0100	3/20/2006	METHOD AND DEVICE FOR PROTECTED TRANSMISSION OF DATA WORDS	Klug, Franz; Kunemund, Thomas; Sonnekalb, Steffen; Wenzel, Andreas

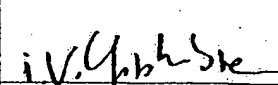| Patent No.	Issue Date	Title	Inventor(s)
*6,774,691	8/10/2004	HIGH RESOLUTION INTERLEAVED DELAY CHAIN	Han, Jonghee; Kim, Jung Pill
5,907,580	5/25/1999	METHOD AND APPARATUS FOR COMMUNICATING INFORMATION	Cummings, Mark R.
6,175,589	1/16/2001	METHOD AND APPARATUS FOR COMMUNICATING INFORMATION	Cummings, Mark R.
6,333,641	12/25/2001	APPLICATION AND METHODS FOR DYNAMICALLY DEFINING VARIABLY SIZED AUTONOMOUS SUB-ARRAYS WITHIN A PROGRAMMABLE GATE ARRAY	Wasson, Stephen L.
6,433,578	8/13/2002	HETEROGENEOUS PROGRAMMABLE GATE ARRAY	Wasson, Stephen L.
6,449,628	9/10/2002	PROGRAMMABLE DATA ARITHMETIC ARRAYS	Wasson, Stephen L.
6,546,261	4/8/2003	METHOD AND APPARATUS FOR CONFIGURING INFORMATION	Cummings, Mark R.
6,567,017	5/20/2003	A CONFIGURABLE CODE GENERATOR SYSTEM FOR SPREAD SPECTRUM APPLICATIONS	Medlock, Joel D.; Chou, Paul L.
6,694,496	2/17/2004	FLEXIBLE PREAMBLE PROCESSING	Balasubramonian, Venugopal; Goslin, Gregory R.
6,895,036	5/17/2005	METHOD AND APPARATUS FOR CORRELATING CODES IN CDMA SYSTEMS	Medlock, Joel D.
6,816,876	11/9/2004	APPARATUS AND METHOD FOR AUGMENTING AN M-SEQUENCE WITH ARBITRARY PHASE SHIFT	Medlock, Joel D.
6,947,468	9/20/2005	APPARATUS AND METHOD FOR GENERATING A FIBONACCI VECTOR FIELD FROM A GALOIS FIELD	Medlock, Joel D.
6,967,999	11/22/2005	METHOD AND APPARATUS TO SUPPORT MULTISTANDARD, MULTISERVICE BASE-STATIONS FOR WIRELESS VOICE AND DATA NETWORKS	Subramanian, Ravi; Rieken, Keith; Woodthrope, Christopher C.
7,010,06	3/7/2006	APPARATUS AND METHOD FOR CONFIGURABLE ALL-DIGIT COHERENT DEMODULATOR SYSTEM FOR WCDMA APPLICATIONS	Subramanian, Ravi
6,934,319	8/23/2005	APPARATUS AND METHOD FOR MULTIMODE DIGITAL DESPREADER FOR CDMA SIGNAL PROCESSING	Subramanian, Ravi
6,665,817	12/16/2003	APPARATUS AND METHOD FOR IMPLEMENTING A WIRELESS SYSTEM ON A CHIP WITH A REPROGRAMMABLE TESTER, DEBUGGER AND BUS MONITOR	Rieken, Keith
6,404,227	6/11/2002	APPARATUS AND METHOD FOR INTERLEAVING A SIGNAL CARRY CHAIN IN AN INTEGRATED CIRCUIT	Wasson, Stephen L.; Varn, David K.; Ralston, John D.
6,611,570	8/26/2003	PROGRAMMABLE DIGITAL INTERMEDIATE FREQUENCY TRANSCEIVER	
6,686,767	2/3/2004	APPARATUS AND METHOD FOR CONTROLLING A THREE-STATE BUS	Wasson, Stephen L.
6,701,431	3/2/2004	A WIRELESS SPREAD SPECTRUM COMMUNICATION PLATFORM USING DYNAMICALLY RECONFIGURABLE LOGIC	Subramanian, Ravi; Jha, Uma; Medlock, Joel D.
6,721,581	4/13/2004	REPROGRAMMABLE DIGITAL WIRELESS COMMUNICATION DEVICE AND METHOD OF OPERATING SAME	Subramanian, Ravi
6,724,833	4/20/2004	METHOD AND APPARATUS FOR COMMUNICATING INFORMATION	Cummings, Mark R.
6,807,155	10/19/2004	METHOD OF PROFILING DISPARATE COMMUNICATIONS AND SIGNAL PROCESSING STANDARDS AND SERVICES	Subramanian, Ravi
6,809,361	10/26/2004	MAGNETIC MEMORY UNIT AND MAGNETIC MEMORY ARRAY	Honlein, Wolfgang; Kreupl, Franz
6,838,922	1/4/2005	CIRCUIT ARRANGEMENT FOR GENERATING NON-OVERLAPPING CLOCK PHASES	Melcher, Gebhard
6,861,872	3/1/2005	VOLTAGE DOWN CONVERTER FOR LOW VOLTAGE OPERATION	Suh, Jungwon
6,873,558	3/29/2005	INTEGRATED CIRCUIT AND METHOD FOR OPERATING THE INTEGRATED CIRCUIT	Kunemund, Thomas; Wenzel, Andreas
6,940,755	9/6/2005	SEMICONDUCTOR WITH AN IMPROVED READ DEVICE AND OPERATIONAL MODE ASSOCIATED THEREWITH	Peters, Christian; Sedlak, Holger
6,946,386	9/20/2005	PROCESS FOR PRODUCING ULTRATHIN HOMOGENOUS	Steinlesberger, Gernot;

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		METAL LAYERS	Engelhardt, Manfred; Unger, Eugen
6,946,878	9/20/2005	INTEGRATED CIRCUIT AND CIRCUIT ARRANGEMENT FOR CONVERTING A SINGLE-RAIL SIGNAL INTO A DUAL- RAIL SIGNAL	Kunemund, Thomas
6,962,294	11/8/2005	INTEGRATED CIRCUIT HAVING AN ACTIVE SHIELD	Beit-Brogger, Andrea; Riegebauer, Josef
6,965,910	11/15/2005	CALCULATING UNIT AND METHOD FOR ADDING	Elbe, Astrid; Janssen, Norbert; Sedlak, Holger; Seifert, Jean-Pierre
6,966,496	11/22/2005	CHIP MODULE	Gundlach, Harald
6,970,016	11/29/2005	DATA PROCESSING CIRCUIT AND METHOD FOR TRANSMITTING DATA	Elbe, Astrid; Janssen, Norbert; Sedlak, Holger
6,970,899	11/29/2005	CALCULATING UNIT AND METHOD FOR SUBTRACTING	Elbe, Astrid; Janssen, Norbert; Sedlak, Holger; Seifert, Jean-Pierre
6,979,859	12/27/2005	FLASH MEMORY CELL AND FABRICATION METHOD	Hofmann, Franz; Specht, Michael
6,982,202	1/3/2006	FABRICATION METHOD FOR MEMORY CELL	Hofmann, Franz; Landgraf, Erhard; Luyken, Hannes
6,984,446	1/10/2006	PROCESS FOR PRODUCING A METAL LAYER ON A SUBSTRATE BODY, AND SUBSTRATE BODY HAVING A METAL LAYER	Gundlach, Harald; Muller-Hipper, Andreas; Simmerlein-Erlbacher, Ewald
6,985,917	1/10/2006	CONFIGURABLE CALCULATING UNIT	Elbe, Astrid; Fischer, Wieland; Janssen, Norbert; Sedlak, Holger
6,998,672	2/14/2006	MEMORY CELL	Hofmann, Franz; Willer, Josef
6,999,333	2/14/2006	METHOD AND APPARATUS FOR ASSESSING ONE-TIME PROGRAMMABLE CELLS	Janke, Marcus
6,999,337	2/14/2006	REGISTER CELL AND METHOD FOR WRITING TO THE REGISTER CELL	Elbe, Astrid; Fischer, Wieland; Janssen, Norbert; Sedlak, Holger; Seifert, Jean-Pierre
7,005,894	11/045,000	ADAPTIVE VOLTAGE MONITORING	Weder, Uwe
7,015,821	3/21/2006	EEPROM MEMORY MATRIX AND METHOD FOR SAFEGUARDING AN EEPROM MEMORY MATRIX	Gamperl, Susanne; Tscheternigg, Siegfried; Wedel, Armin
7,016,927	3/21/2006	METHOD AND APPARATUS FOR MODULAR MULTIPLICATION	Elbe, Astrid; Janssen, Norbert; Sedlak, Holger; Seifert, Jean-Pierre
7,016,929	3/21/2006	METHOD AND DEVICE FOR CALCULATING A RESULT OF AN EXPONENTIATION	Fischer, Wieland; Seifert, Jean-Pierre

Signature of Assignee of Record

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Henriette Post Patent Administrator	Date 04/19/2006	Susanne Girstenbreu Patent Administrator	Date 04/19/2006